



Entry+ module EVK hardware user guide



Entry+ module EVK hardware user guide
- XE125

Author: Acconeer

Version 1.0: 2023-07-03

Acconeer AB



Table of Contents

- 1. Overview of the XE125 Entry+ Module Evaluation Kit..... 4
 - 1.1. Introduction 4
 - 1.2. Getting Started..... 5
- 2. Software for the EVK..... 6
 - 2.1. SW download 6
 - 2.2. SW API Description..... 6
- 3. The EVK Hardware..... 7
 - 3.1. XE125 Evaluation Board..... 9
 - 3.1.1. Overview 9
 - 3.1.2. Power..... 9
 - 3.1.3. Not Mounted Components 10
 - 3.1.4. Electrical Schematic 11
 - 3.1.5. Bill of Material 15
 - 3.1.6. Component Placement Drawing..... 15
 - 3.1.7. Connectors..... 17
 - 3.2. XM125 Entry+ Module..... 19
 - 3.2.1. Overview 19
 - Electrical Schematic 20
 - 3.2.2. Bill of Material 22
 - 3.2.3. Land Grid Array 22
- 4. Safety..... 24
 - 4.1. Electrostatic precautions..... 24
- 5. Regulatory Information 25
- 6. Revision History..... 26
- 7. Disclaimer 27



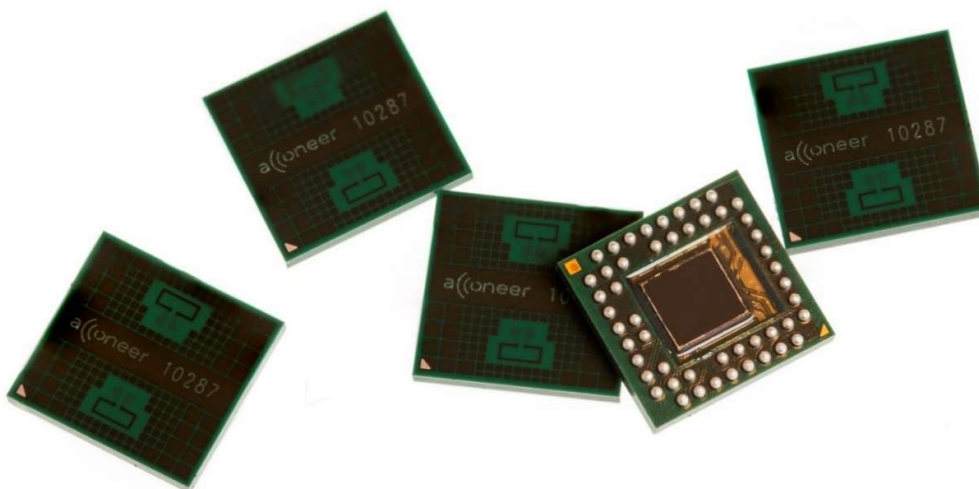
1. Overview of the XE125 Entry+ Module Evaluation Kit

1.1. Introduction

The XE125 Entry+ Module Evaluation Kit (The EVK) is a development platform targeting straightforward use cases where small size, low cost and low power is key.

The EVK features Acconeer's XM125 Entry+ module, including the A121 radar sensor. The A121 radar sensor is an optimized low-power, high-precision 60 GHz radar with antenna in package (AiP) and integrated baseband. Together with the ARM® Cortex®-M4 STM32L431 MCU, the XM125 Entry+ module becomes a cost- and size-optimized low-power radar sensor.

The A121 is based on pulsed coherent radar technology (PCR). It has leading-edge patented sensor technology with pico-second time resolution. The A121 shows unprecedented performance as far as power consumption and distance accuracy are concerned and it comes fully integrated in a small package of 29 mm².



The A121 can measure absolute distance with mm accuracy up to a range of 20 m depending on the use case.

The A121, 60 GHz radar is not compromised by natural sources of interference such as noise, dust, color, direct or indirect light.

The EVK consists of:

- 1 XE125 Evaluation board with an XM125 Entry+ module soldered onto it.

The XE125 is compatible with Acconeer LH132 lens kit (LH132 is also compatible with evaluation kit XE132). LH132 is sold separately.



1.2. Getting Started

A Quick Installation Guide is available at <https://youtu.be/Z8lQgxaJFOY>

This short instruction video will ensure a smooth setup and installation. As an alternative you can also find a guide in pdf-format at <https://developer.acconeer.com>.



2. Software for the EVK

2.1. SW download

The SW is available for download at <https://developer.acconeer.com>. SW User Guides can be downloaded at the same site.

2.2. SW API Description

The Acconeer SW comes with an API (Application Programming Interface). Acconeer provides several service-oriented example and reference applications, as well as user guidelines for application development when utilizing the API. All APIs provided by Acconeer are documented.

Unzip the SW zip file downloaded from Acconeer's download site. In the file structure, please locate /doc folder from where API documentation in HTML format is found at doc/html/index.html.



3. The EVK Hardware

In Figure 1 the block-diagram of the XE125 is shown. Figure 2 shows the XM125 block-diagram.

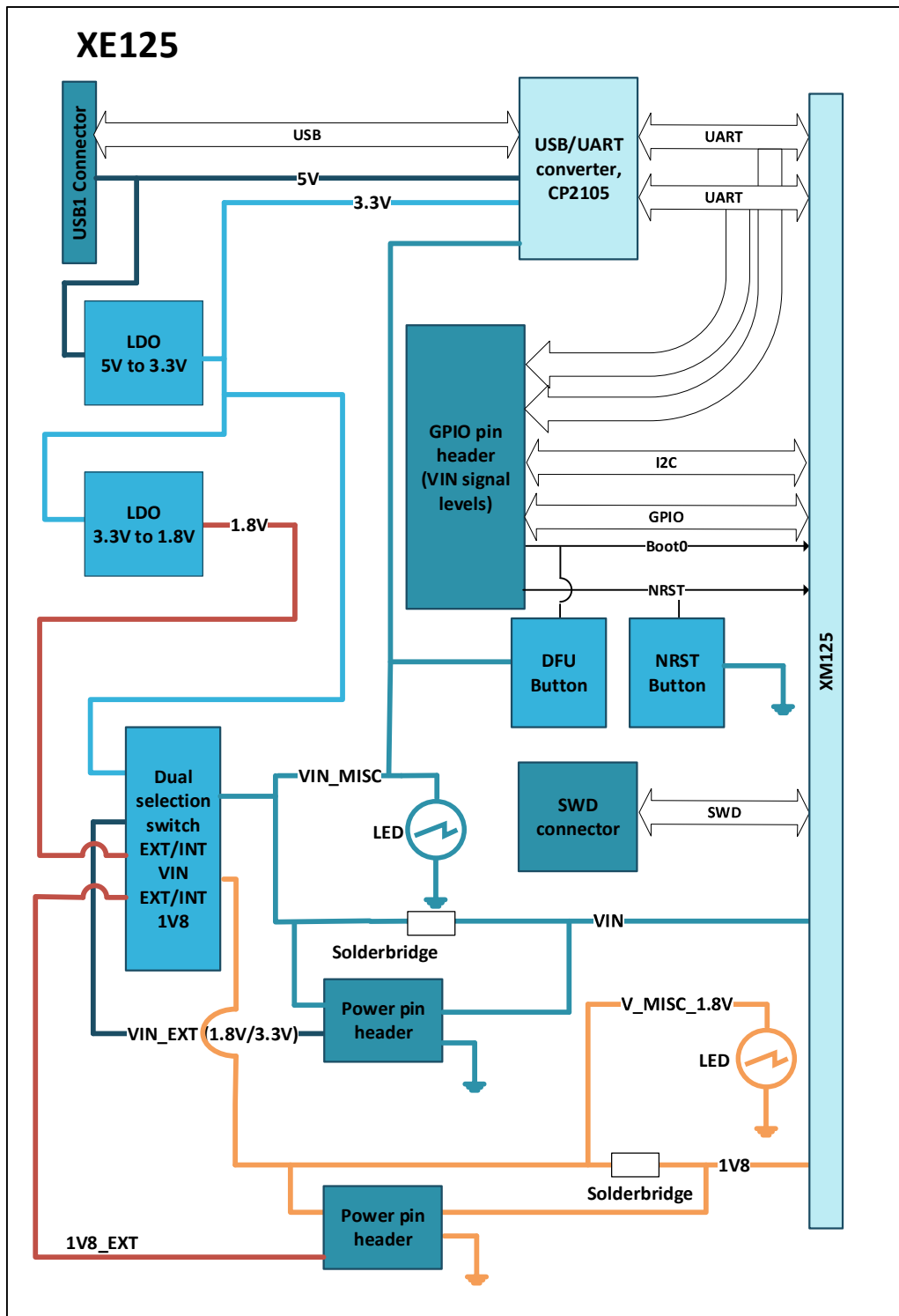


Figure 1 The block-diagram of the XE125.

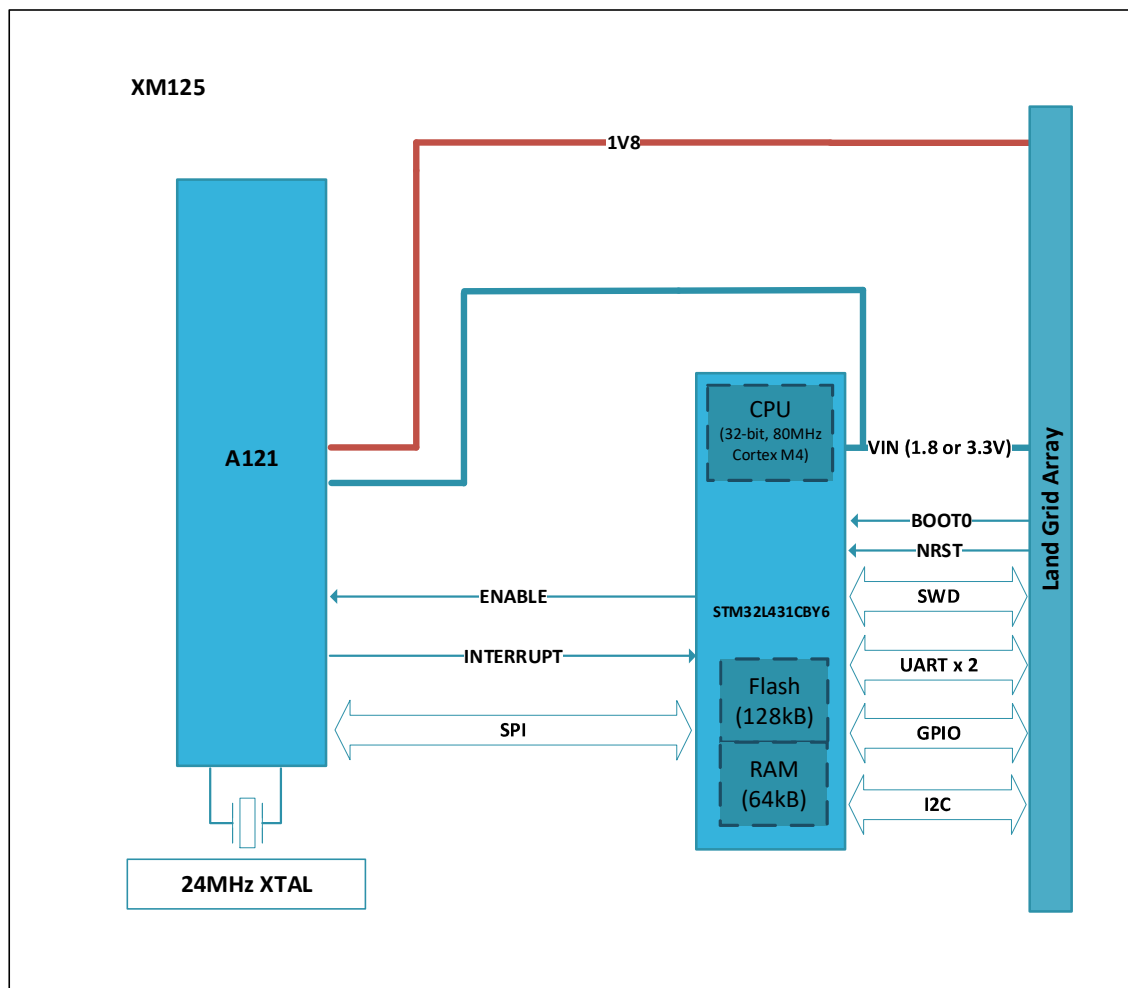


Figure 2. The block-diagram of XM125.



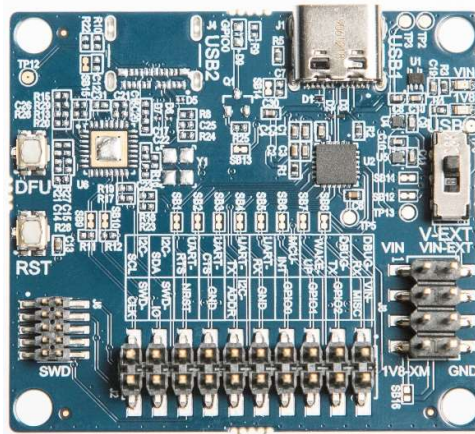
3.1. XE125 Evaluation Board

3.1.1. Overview

The XE125 is an evaluation board including the XM125 Entry+ module. It makes the interfaces from the XM125 module accessible for evaluation and debug. It also enables flashing of the XM125 via USB-UART or SW-DP. The XM125 Entry+ module is included in the XE125 Evaluation board. In Picture 1 you will find the XE125 front side where the XM125 is mounted. Picture 2 shows the back side of XE125.



Picture 1. The XE125 top side where XM125 is mounted.



Picture 2. The XE125 back side.

3.1.2. Power

In the text below, the reference designators refer to the Electrical Schematic of XE125 in chapter 3.1.4.

The XE125 is powered via the USB connector J1 and/or via the pin header J8. The USB 5V power domain supplies the USB-UART chip (U2). If the USB-UART interface is not used, a dedicated USB charger can be used.



The XM125 module mounted on the XE125 can be powered either from on-board linear regulators U4 (3.3V LDO) and U5 (1.8V LDO) which are supplied from the USB 5V power domain or from “VIN_EXT” and 1V8_EXT in the J8 pin header. VIN_EXT can be either 1.8V or 3.3V.

The power source for XM125 is determined by the setting of the switch “SW3”. When the LEDs D4 and D8 on the XE125 are lit, both power domains (VIN and 1V8) of the XM125 are powered.

It should be noted that regardless whether “VIN_EXT” or the internal LDOs on XE125 are used to power XM125, the LEDs D4 and D8 will consume power since they are connected to “VIN_MISC” and V_MISC_1V8 respectively. If one wants to measure only the current consumed by XM125, power should be supplied to XE125 via the pins “VIN_XM125” and 1V8_XM125 (pins 3 and 7 in pin header J8). In this case, the solder-bridges “SB11” and “SB12” should be cut. This way, all components on XE125 are supplied from the on-board LDOs, but XM125 is supplied from the external power supplies. D4 and D8 will not indicate if XM125 is powered after SB11 and SB12 have been cut.

The intention of the power pin header J8 is that it can be used both for supplying power to XM125 and for measuring current over a measurement resistor. SB11 and SB12 are short-circuited solder-bridges, but the user can change it to a low-value measurement resistor with high accuracy to perform very accurate measurements of the XM125 current. See Table 2 for the pin assignment of pin header J8.

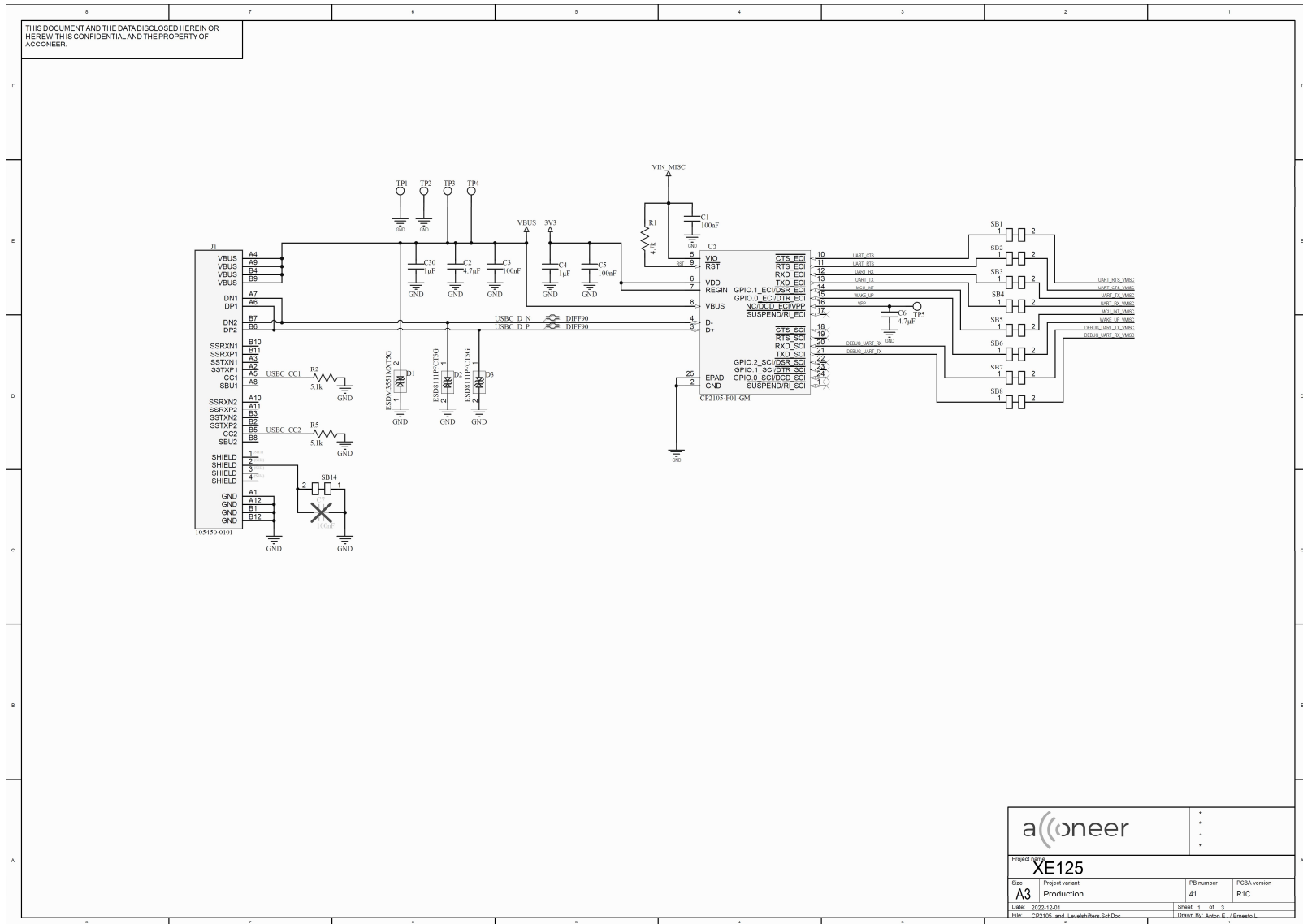
3.1.3. Not Mounted Components

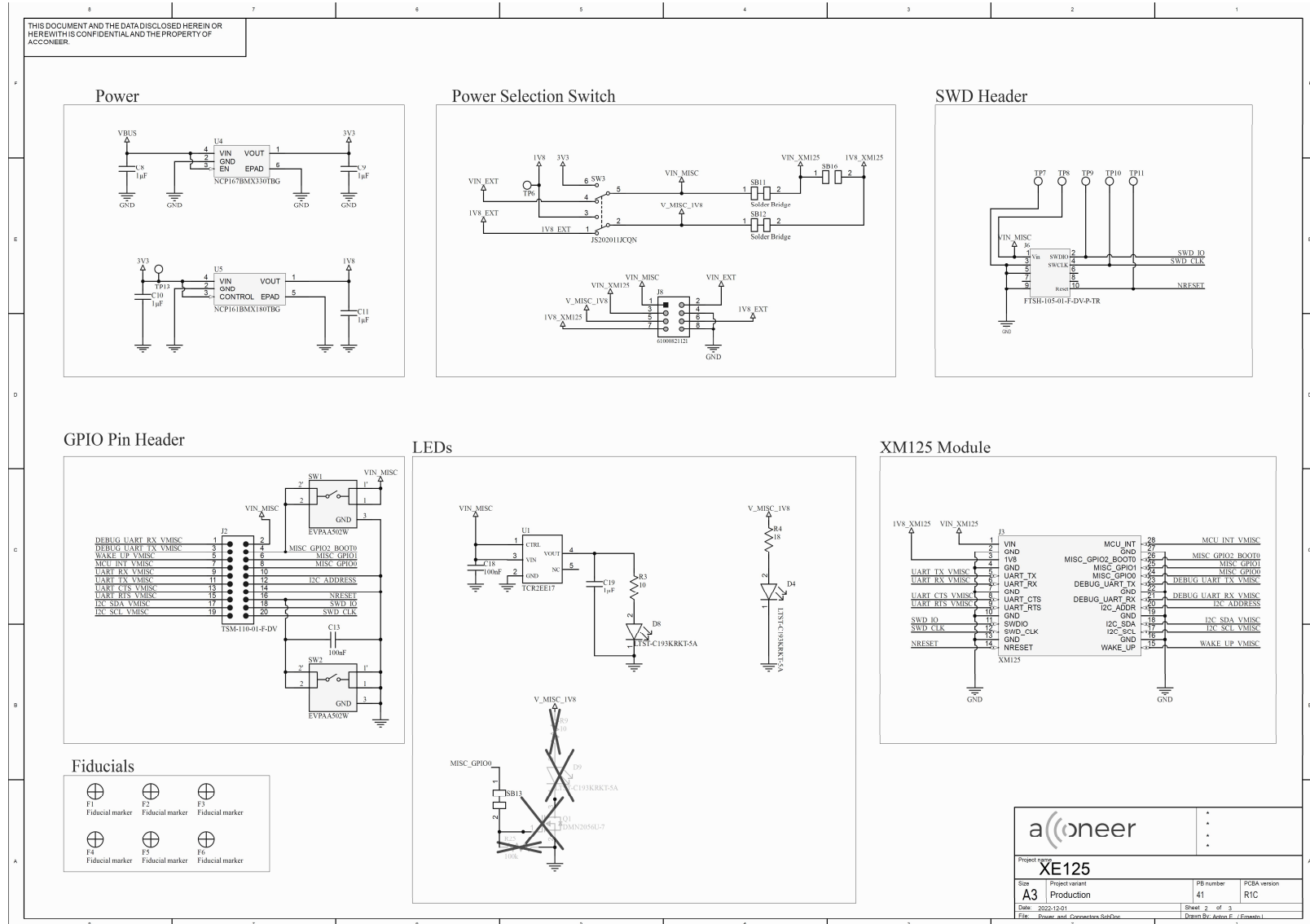
As can be seen in the schematic in chapter 3.1.4, USB connector J4 and several other components are “No Mount”. These components are related to the FTDI I2C-USB bridge chip FT4222H which can be mounted on the PCB. It was added for internal Acconeer use. The I2C interface is also accessible in pin header J2. Refer to Table 4.

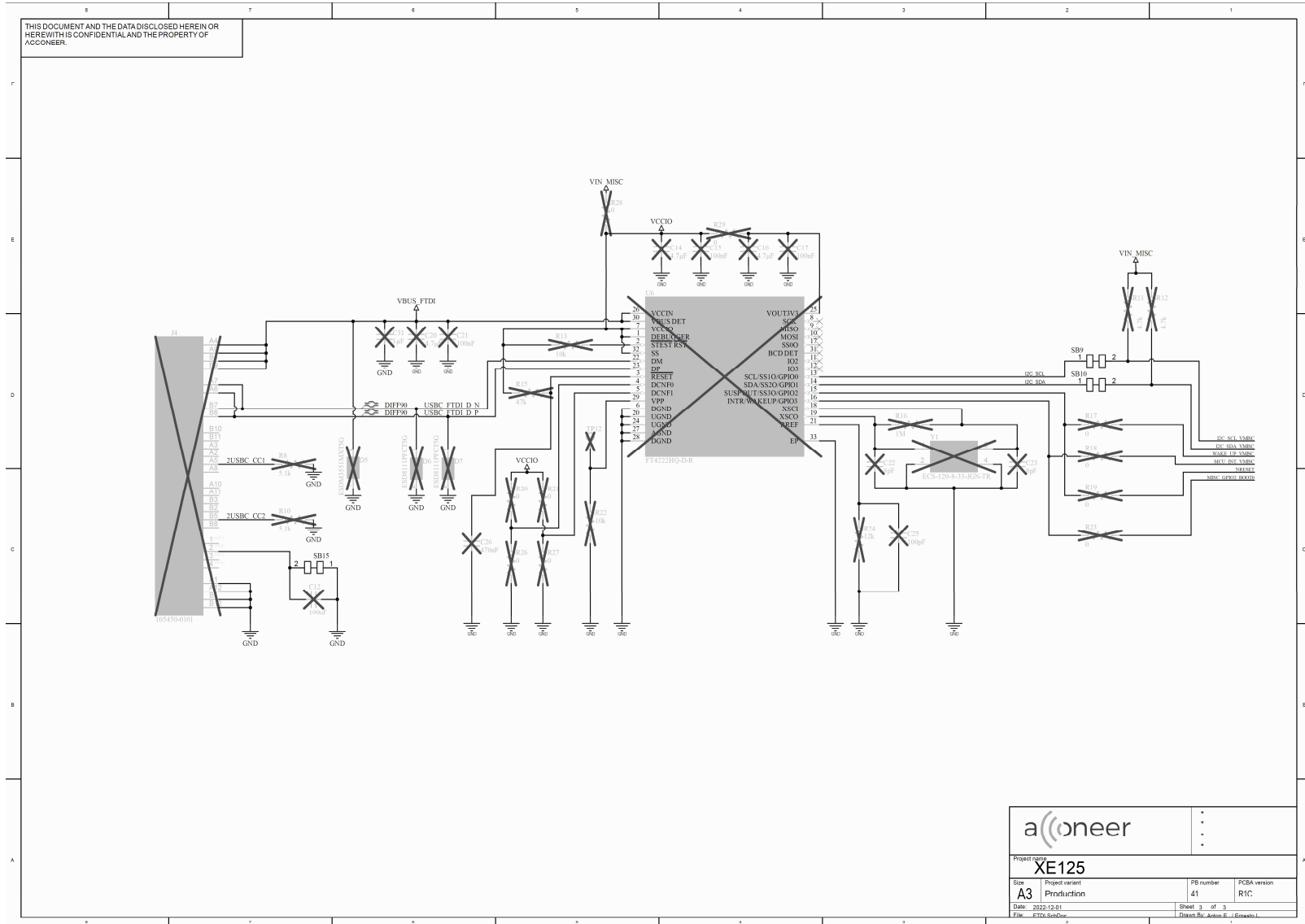


3.1.4. Electrical Schematic

On the following pages, please find the Electrical Schematic for XE125:









3.1.5. Bill of Material

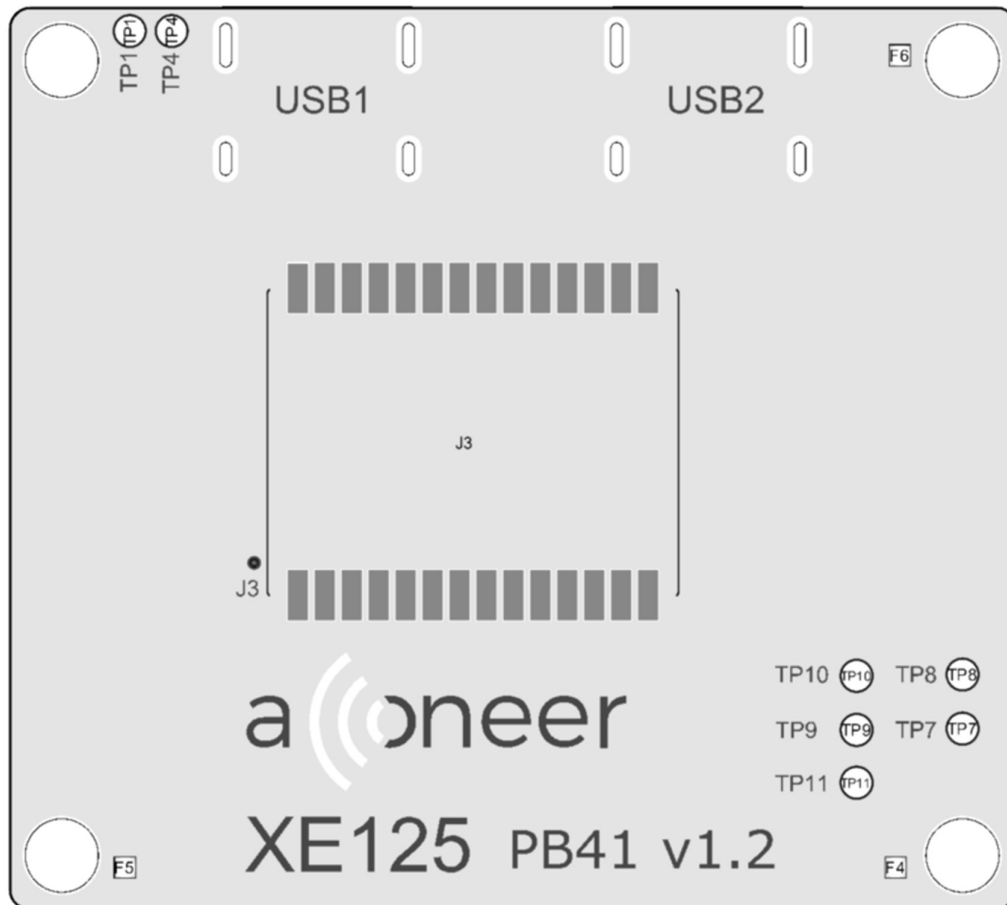
Table 1 shows the BOM for the XE125

Table 1 The BOM for the XE125.

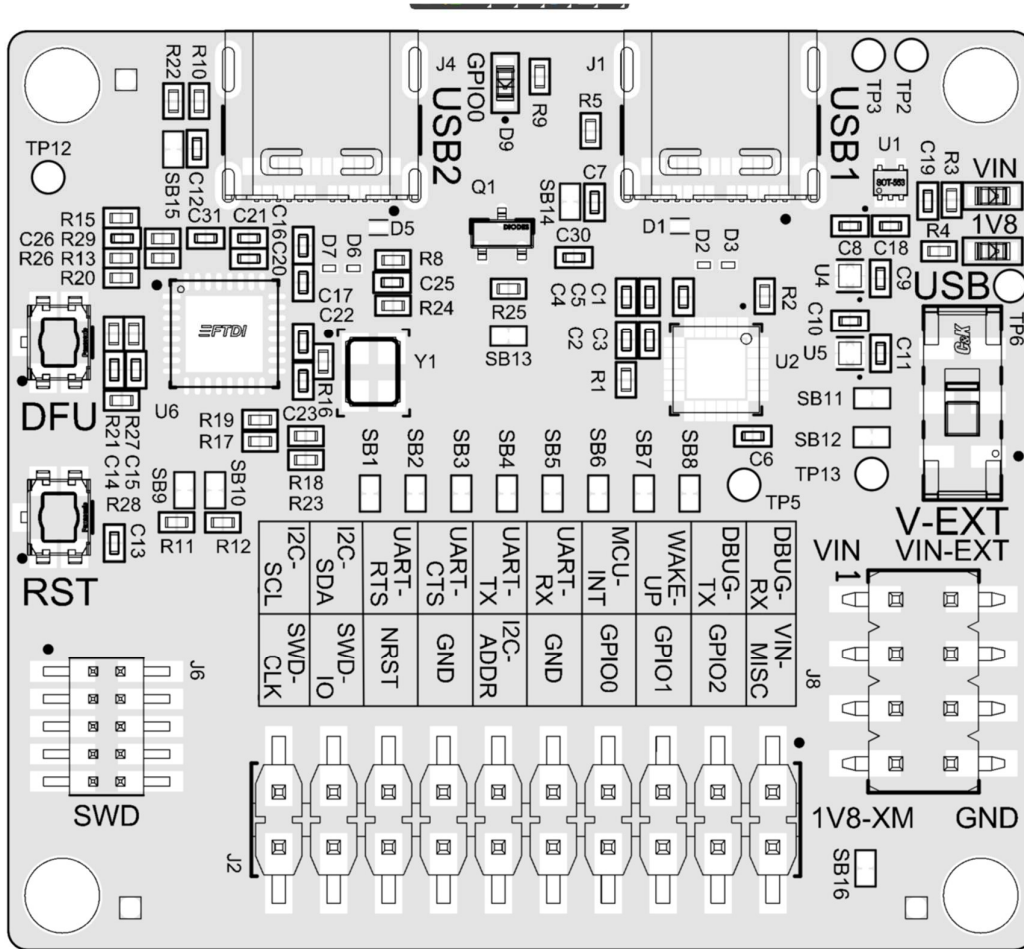
Designator	Name	Qty	Value	Tolerance	Voltage rating	Manufacturer
C4, C8, C9, C10, C11, C19, C30	Capacitor 1uF, metric 1005	7	1μF	20%	10V	
C2, C6	Capacitor 4.7uF, metric 1005	2	4.7μF	20%	10V	
C1, C3, C5, C13, C18	Capacitor 100nF, metric 1005	5	100nF	20%	10V	
R1	Resistor 4.7 kOhm, metric 1005	1	4.7k	1%		
R2, R5	Resistor 5.1 KOhm, metric 1005	2	5.1k	1%		
R3	Resistor 10 Ohm, metric 1005	1	10	1%		
R4	Resistor 18 Ohm, metric 1005	1	18	1%		
D1	ESDM3551MXT5G	1				ON Semiconductor
D2, D3	ESD8111PFCT5G	2				ON Semiconductor
D4, D8	LTST-C193KRKT-5A	2				Vishay Lite-On
SW1, SW2	EVPA502W	2				Panasonic
SW3	JS202011JCQN	1				ITT C&K
U1	TCR2EE17	1				Toshiba Semiconductor
U2	CP2105-F01-GM	1				Silicon Labs
U4	NCP167BMX330TBG	1				ON Semiconductor
U5	NCP161BMX180TBG	1				ON Semiconductor
J1	105450-0101	1				Molex
J2	TSM-110-01-F-DV	1				Samtec
J3	XM125	1				
J6	FTSH-105-01-F-DV-P-TR	1				Samtec
J8	61000821121	1				Würth Electronics

3.1.6. Component Placement Drawing

In Picture 3 and Picture 4 the component placement drawing of XE125, top and bottom side, are found:



Picture 3. The component placement of XE125 top side.



Picture 4. The component placement of XE125 bottom side.

3.1.7. Connectors

3.1.7.1. Power pin header (J8)

The power pin header J8 provides the possibility to supply the XM125 module with power from external power supplies. In Table 2, the pin assignment of J8 is shown:

Table 2. The pin assignment of the power pin header J8.

Pin Number	Signal	Pin Number	Signal
1	VIN_MISC	2	VIN_EXT
3	VIN_XM125	4	GND
5	V_MISC_1V8	6	1V8_EXT
7	1V8_XM125	8	GND

3.1.7.2. 2x5 JTAG/SWD pin header (J6)

The 2x5 JTAG/SWD pin header (1.27mm pitch) contains the signals needed for flashing the XM125 MCU via the SWD interface. The pinout matches that of the Cortex 10-pin JTAG/SWD Connector and is found in Table 3.



Table 3. The pinout of J6.

Pin Number	Signal	Pin Number	Signal
1	VIN	2	SWD_IO
3	GND	4	SWD_CLK
5	GND	6	NC (no TRACESWO available)
7	NC	8	NC
9	GND	10	NRESET

3.1.7.3. 2x10 pin header (J2)

The 2x10 pin header (2.54mm pitch) contains the interface and GPIOs from the XM125. All signals are on the VIN_MISC voltage level (3.3V if on-board LDO used, otherwise VIN = 1.8 or 3.3V). The pinout is found in Table 4.

Table 4. The pinout of J2.

Pin Number	Signal	Pin Number	Signal
1	DEBUG_UART_RX ¹	2	VIN_MISC
3	DEBUG_UART_TX ²	4	MISC_GPIO2_BOOT0
5	WAKE_UP	6	MISC_GPIO1
7	MCU_INT	8	MISC_GPIO0
9	UART_RX ¹	10	GND
11	UART_TX ²	12	I2C_ADDRESS
13	UART_CTS ³	14	GND
15	UART_RTS ⁴	16	NRESET
17	I2C_SDA	18	SWD_IO
19	I2C_SCL	20	SWD_CLK

3.1.7.4. Switches and buttons

There is one switch on XE125. SW3 determines if XM125 is powered from the XE125 on-board LDOs (U4 and U5) or from external power supplies via pin header J8 (VIN_EXT/1V8_EXT).

There are two buttons on the XE125. SW1 controls the signal “BOOT0” connected to XM125 and SW2 controls “NRESET” connected to the XM125. In Table 5 the state of the buttons and the corresponding signal states are listed.

¹ UART_RX is connected to UART_RX on XM125. If external UART device is connected, this pin should be connected to TX of external device.

² UART_TX is connected to UART_TX on XM125. If external UART device is connected, this pin should be connected to RX of external device.

³ UART_CTS is connected to UART_CTS on XM125. If external UART device is connected, this pin should be connected to RTS of external device.

⁴ UART_RTS is connected to UART_RTS on XM125. If external UART device is connected, this pin should be connected to CTS of external device.



Table 5. The states of the buttons SW1 and SW2.

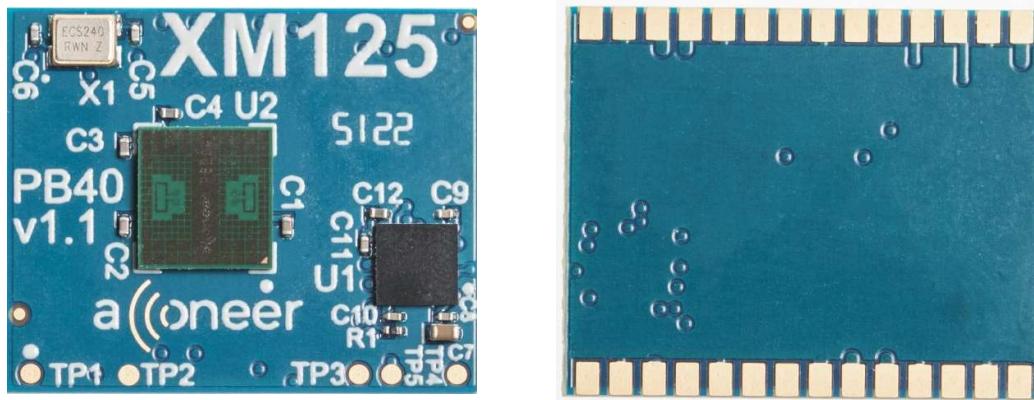
Button	Open (default)	Closed
SW1	BOOT0=0	BOOT0=1
SW2	NRST=1	NRST=0

3.2. XM125 Entry+ Module

3.2.1. Overview

The XM125 Entry+ Module is included in the XE125 design and soldered on the top side of the XE125 via a Land Grid Array pattern on the bottom side of the PCB. In

Picture 5 below the top and bottom side of XM125 is shown.



Picture 5. The top and bottom side of XM125.



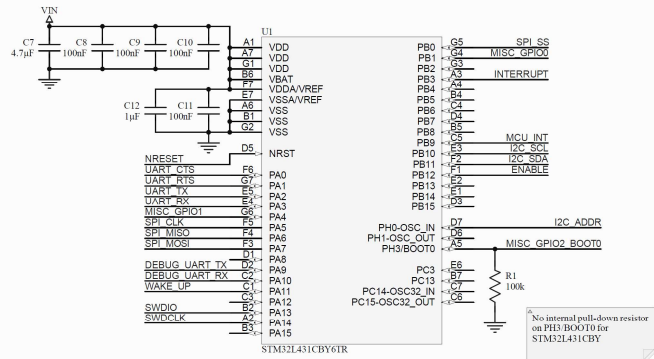
Electrical Schematic

On the following pages, please find the Electrical Schematic for XM125:

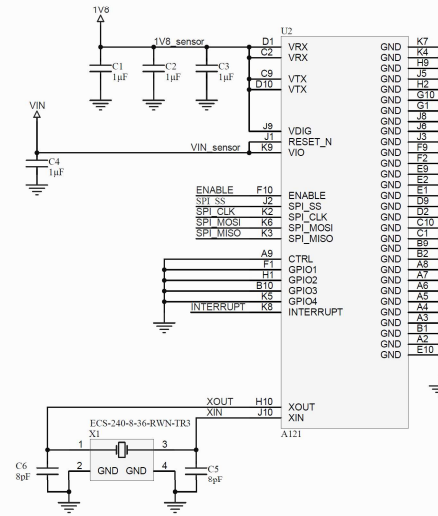


THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR HEREWITH IS CONFIDENTIAL AND THE PROPERTY OF ACCONEER.

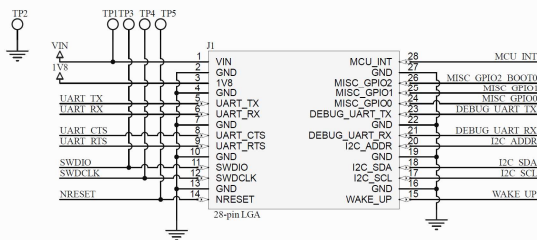
MCU



Radar sensor



Module LGA footprint



		Acconeer AB Västra Varvsgatan 19 211 77 Malmö Sweden	
Project name: XM125.PrjPcb			
Size: A3	Project variant: Production	FB number: 40	PCBA version: R1B
Date: 2022-11-10		Sheet 1 of 1	
File: XM125_SchDoc		Drawn By: Josefa Lindbäck	



3.2.2. Bill of Material

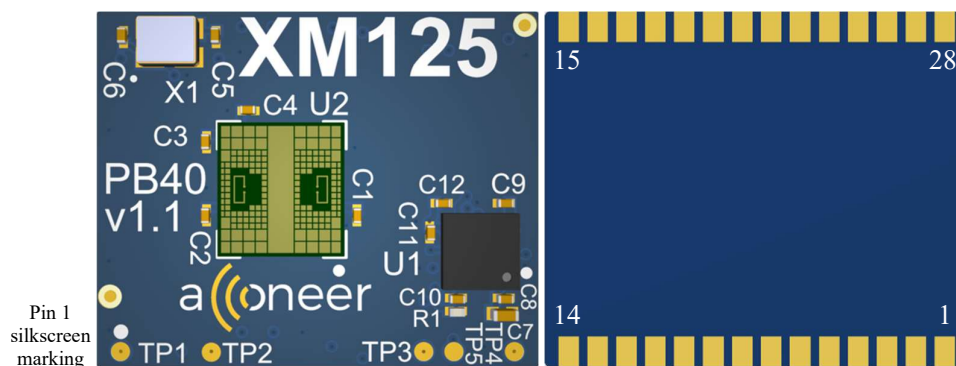
Table 6 shows the BOM for the XM125.

Table 6. The BOM For XM125.

Designator	Name	Qty	Value	Comment
C1, C2, C3, C4, C12	Capacitor 1 uF, metric 0603	5	1 μ F	
C5, C6	Capacitor 8 pF, metric 0603	2	8 pF	
C7	Capacitor 4.7uF, metric 1005	1	4.7 μ F	
C8, C9, C10, C11	Capacitor 100nF, metric 0603	4	100 nF	
R1	Resistor 100 kOhm, metric 0603	1	100 kOhm	
U1	MCU STM32L431CBY6 32-bit WLCSP49	1		ST Microelectronics: STM32L431CBY6
U2	Radar Sensor 60 GHz A121	1		Acconeer AB: A121
X1	Crystal 24MHz 9 pF 4-Pin SMD	1	24 MHz	ECS-240-8-36-RWN

3.2.3. Land Grid Array

Picture 6 shows the XM125 module front and back side pin markings. Table 7 shows the module pinout.



Picture 6. The pin marking of XM125.

Table 7. XM125 LGA pinout.

Pin Number	Signal	Comment
1	VIN	1.8 V or 3.3 V input A121 IO and MCU voltage
2	Ground	
3	1V8	1.8 V input A121 analog and digital voltage
4	Ground	



5	UART_TX	Connect to UART_RX on host side. Leave Not Connected if unused.
6	UART_RX	Connect to UART_TX on host side. Leave Not Connected if unused.
7	Ground	
8	UART_CTS	Connect to UART_RTS on host side. Leave Not Connected if unused.
9	UART_RTS	Connect to UART_CTS on host side. Leave Not Connected if unused.
10	Ground	
11	SWD_IO	Leave Not Connected if unused.
12	SWD_CLK	Leave Not Connected if unused.
13	Ground	
14	NRESET	Reset. Leave Not Connected if unused.
15	WAKE_UP	Could be used by host to wake up XM125 MCU. Leave Not Connected if unused.
16	Ground	
17	I2C_SCL	Leave Not Connected if unused.
18	I2C_SDA	Leave Not Connected if unused.
19	Ground	
20	I2C_ADDRESS	For configuration of I2C address. Leave Not Connected if unused.
21	DEBUG_UART_RX	Connect to UART_TX on host side. Leave Not Connected if unused.
22	Ground	
23	DEBUG_UART_TX	Connect to UART_RX on host side. Leave Not Connected if unused.
24	MISC_GPIO0	Leave Not Connected if unused.
25	MISC_GPIO1	Leave Not Connected if unused.
26	MISC_GPIO2_BOOT0	Pulling BOOT0 high during boot of module will start the embedded boot loader. Leave Not Connected if unused.
27	Ground	
28	MCU_INT	Could be used to send interrupt from MCU to host. Leave Not Connected if unused.



4. Safety

4.1. Electrostatic precautions



Please take electrostatic precautions, including using ground straps, when using the EVK or any of its components. An electrostatic discharge could damage the device.



5. Regulatory Information

For regulatory compliance of XM125, refer to XM125 datasheet:

<https://developer.acconeer.com>.

Independent of XM125 regulatory status it is the user's responsibility to ensure that any regulatory requirements, applicable to any region, are followed in the region the device is being used.

Regulatory Compliance for A121, refer to A121 datasheet:

<https://developer.acconeer.com/download/a121-datasheet-pdf/>

Independent of A121 regulatory status it is the user's responsibility to ensure that any regulatory requirements, applicable to any region, are followed in the region the device is being used.



6. Revision History

Date	Revision	Changes
2023-03-29	1.0	Original version
2023-07-03	1.1	Updated Table 4 (corrected error).



7. Disclaimer

The information herein is believed to be correct as of the date issued. Acconeer AB (“**Acconeer**”) will not be responsible for damages of any nature resulting from the use or reliance upon the information contained herein. Acconeer makes no warranties, expressed or implied, of merchantability or fitness for a particular purpose or course of performance or usage of trade. Therefore, it is the user’s responsibility to thoroughly test the product in their particular application to determine its performance, efficacy and safety. Users should obtain the latest relevant information before placing orders.

Unless Acconeer has explicitly designated an individual Acconeer product as meeting the requirement of a particular industry standard, Acconeer is not responsible for any failure to meet such industry standard requirements.

Unless explicitly stated herein this document Acconeer has not performed any regulatory conformity test. It is the user’s responsibility to assure that necessary regulatory conditions are met and approvals have been obtained when using the product. Regardless of whether the product has passed any conformity test, this document does not constitute any regulatory approval of the user’s product or application using Acconeer’s product.

Nothing contained herein is to be considered as permission or a recommendation to infringe any patent or any other intellectual property right. No license, express or implied, to any intellectual property right is granted by Acconeer herein.

Acconeer reserves the right to at any time correct, change, amend, enhance, modify, and improve this document and/or Acconeer products without notice.

This document supersedes and replaces all information supplied prior to the publication hereof.



Acconeer AB
Västra Varvsgatan 19
211 77 Malmö
Sweden

www.acconeer.com
info@acconeer.com
+46 10 218 92 00